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10575676 - GAU: 2624

PTO/58/08a (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet

1

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7

**Complete if Known**

Application Number	10/575,676
Filing Date	October 25, 2006
First Named Inventor	Jill MacDonald Boyce
Art Unit	2624
Examiner Name	Anad P. Bhatnagar
Attorney Docket Number	PU030282

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. *	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		US-6560816	08-27-1996	Cloonan et al	
		US-6659382	08-19-1997	Rybczynski	
		US-5917609	06-29-1999	Brescower et al	
		US-6213838	04-17-2001	Cherichetti et al	
		US-6269180	07-31-2001	Sevigny	
		US-6373992	04-16-2002	Nagao	
		US-6459599	10-06-2002	Kimura et al	
		US-6587609	07-01-2003	Suzuki et al	
		US-7065255	06-20-2006	Chen et al	
		US-7738722	06-19-2010	Gomila et al	
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		US-2002/171549	11-21-2002	Fogg	
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		US-2006/0082649	04-20-2006	Gomila et al	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. *	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
		EP 0364285	04-18-1990	M.I.T		
		EP0575006	12-22-1993	Kodak		
		EP0756281	01-25-1997	Sony		
		EP 1511328	03-02-2005	Matsushita		
		WO 00/18109	03-31-2000	Philips		
		WO 02/33958	04-25-2002	Kodak		
		WO 97/10675	03-26-1997	Philips		
		WO 2004/034518	04-14-2004	Thomson		
		WO 2004/095829	11-04-2004	Thomson		

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Considered

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Sheet

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Application Number	10/575,676
Filing Date	October 25, 2006
First Named Inventor	Jill MacDonald Boyce
Art Unit	2624
Examiner Name	Anad P. Bhatnagar
Attorney Docket Number	PU030282

**U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		US-2006/0083316	04-20-2006	Cooper et al	
		US-2006/0083425	04-20-2006	Cooper et al	
		US-2006/0132193	08-17-2006	Winger	
		US-2006/0291667	12-28-2006	Tourapis	
		US-2006/0133686	06-22-2006	Gomila et al	
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		US-2007/0104380	05-10-2007	Gomila et al	
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		US- 6496321	12-17-2002	Wolf et al	
		US- 6895793	02-07-2006	Albadawi et al	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	7 <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
		WO 2005/027045	03-24-2005	Thomson		
		WO 2006/039189	04-28-2006	Thomson		
		WO 2006/057703	06-01-2006	Thomson		
		JP 4-097681	03-30-1992	Nippon T & T		Abstract
		JP 8-079765	03-23-1995	Sony		Abstract
		JP 8-149488	06-07-1996	Sony		MT
		JP 9-062718	03-07-1997	Sony		Abstract
		JP 2881/357090	12-26-2001	Hitachi Ltd	=US2001058636	

Examiner  
Signature

/Anand Bhatnagar/

Date  
Considered

01/17/2012

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Sheet 3 of 7

**Complete if Known**

Application Number	10/575,676
Filing Date	October 25, 2006
First Named Inventor	Jill MacDonald Boyce
Art Unit	2624
Examiner Name	Anad P. Bhatnagar
Attorney Docket Number	PU030282

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
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Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>4</sup> - Number <sup>5</sup> - Kind Code <sup>6</sup> (if known)				
		JP 2002/355845	12-13-2005	Matsushita		MT
		JP 2007/529948	10-26-2007	Philips	=US2008/0262781	

Examiner Signature	/Anand Bhatnagar/	Date Considered	01/17/2012
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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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Sheet 4 of 7

## **Complete if Known**

Application Number	10/575,676
Filing Date	October 25, 2006
First Named Inventor	Jill MacDonald Boyce
Art Unit	2624
Examiner Name	Anand P. Bhatnagar
Attorney Docket Number	PU030282

## **NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Office Action from SN 10/552,179 mailed September 1, 2010	
		Office Action from SN 10/556,833 mailed May 10, 2010	
		Final Office Action from SN 10/556,833 mailed October 20, 2010	
		Office Action from SN 10/556,833 mailed January 7, 2011	
		Office Action from SN 10/556,833 mailed February 15, 2011	
		Office Action from SN 10/556,833 mailed May 10, 2011	
		Office Action from SN 10/556,833 mailed September 30, 2011	
		Final Office Action from SN 10/571,148 mailed May 12, 2010	
		Office Action from SN 10/571,148 mailed September 10, 2010	
		Office Action from SN 10/572,820 mailed March 15, 2010	
		Office Action from SN 10/572,820 mailed May 11, 2010	

Examiner Signature	/Anand Bhatnagar/	Date Considered	01/17/2012
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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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Sheet 5 of 7

**Complete if Known**

Application Number	10/575,676
Filing Date	October 25, 2006
First Named Inventor	Jill Macdonald Boyce
Art Unit	2624
Examiner Name	Anad P. Bhatnagar
Attorney Docket Number	PU030282

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		Final Office Action from SN 10/572,920 mailed September 30, 2010	
		Office Action from SN 11/246,848 mailed July 5, 2010	
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		Final Office Action from SN 11/268,070 November 12, 2010	
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		Office Action from SN 11/268,070 mailed June 22, 2011	
		Final Office Action from SN 11/273,067 mailed August 4, 2010	
		Office Action from SN 11/284,378 mailed August 31, 2010	
		Office Action from SN 11/284,378 mailed December 22, 2010	
		Office Action from SN 11/667,581 mailed June 8, 2011	

Examiner Signature	/Anand Bhatnagar/	Date Considered	01/17/2012
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Sheet 6 of 7

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Application Number	10/575,676
Filing Date	October 25, 2006
First Named Inventor	Jill MacDonald Boyce
Art Unit	2624
Examiner Name	Anad P. Bhatnagar
Attorney Docket Number	PU030262

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		Final Office Action from SN 11/667,581 mailed October 14, 2011	
		Office Action from SN 11/667,629 mailed July 9, 2010	
		Final Office Action from SN 11/667,629 mailed November 19, 2010	
		Office Action from SN 11/667,629 mailed January 13, 2011	
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		Office Action from SN 11/667,816 mailed September 15, 2011	
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		Office Action from SN 11/667,846 mailed October 4, 2011	
		Office Action from SN 12/589,217 mailed July 13, 2010	
		Final Office Action from SN 12/589,217 mailed October 28, 2010	
		Office Action from SN 12/589,217 December 2, 2010	

Examiner Signature	/Anand Bhatnagar/	Date Considered	01/17/2012
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		GOMILA et al. "Film Grain Modeling vs. Encoding", May 23-27, 2003, JVT of ISO/IEC MPEG & ITU-T VCEG 11 <sup>th</sup> Meeting, Munich, DE, March 15-19, 2004	
		CONKLIN et al. "Dithering 5-Tap Filter for Intraop Deblocking", JVT of ISO/IEC MPEG & ITU-T VCEG, 3 <sup>rd</sup> Meeting, Fairfax, Virginia, May 6-10, 2002, pages 1-16.	
		BYUN et al. "Power Efficient MPEG-4 Decoder Featuring Low-Complexity Error Resilience", ASIC, 2002 Proceedings, 2002 IEEE ASIA-PACIFIC CONFERENCE on August 6-8, 2002, Piscataway, NJ USA, IEEE	
		TAKASHI et al. "A 60-MW/MPEG4 VIDEO CODEC Using Clustered Voltage Scaling with Variable Supply-Voltage Scheme", IEEE Journal of Solid-State Circuits, IEEE Service Center, Piscataway, NJ USA, vol. 33, no. 11, November 1998, pages 1772-1778.	
		PURI et al. "Video Coding Using the H.264/MPEG-4 AVC Compression Standard", Signal Processing Image Communication, Elsevier Science Publishers, Amsterdam, NL, vol. 19, no. 9, October 1, 2004, pages 7993-848.	
		PIRSCH et al. "VLSI Architectures for Video Compression-A Survey", Proceedings of the IEEE, IEEE, New York, USA, vol. 83, no. 2, February 1, 1995, pages 220-246	

Examiner  
Signature

/Anand Bhatnagar/

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